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FORM PTO-1449	SERIAL NO. 10/676,862	CASE NO. 10519-112
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE September 30, 2003	GROUP ART UNIT
(use several sheets if necessary)	APPLICANT(S): So et al.	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER Number-Kind Code (if known)	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
DL	A1	5,784,328	07/1998	Irrinki et al.		
	A2	5,149,199	09/1992	Kinoshita et al.		
	A3	5,977,746	11/1999	Hershberger et al.		
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David Lam	2/05

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE September 30, 2003	GROUP ART UNIT
(use several sheets if necessary)	APPLICANT(S): So et al.	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER Number-Kind Code (if known)	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
DL	A37	4,698,788	10/06/87	Flannagan et al.		
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	A51	"64M x 8 Bit NAND Flash Memory," Samsung Electronics (October 27, 2000).
	A52	"How Flash Memory Works," wysiwyg://8/http://www.howstuffworks.com/flash-memory.htm?printable=1, 5 pages (1998).
	A53	"Datalight FlashFX™ 4.06 User's Guide," page 11 (August 2000).
	A54	"How Does TrueFFS® manage Wear Leveling?," http://www.m-sys.com/content/information/calcInfo.asp, 2 pages (printed October 5, 2001)
	A55	"A CMOS Bandgap Reference Circuit with Sub-1V Operation," IEEE Journal of Solid-State Circuits, Vol. 34, No. 5, May 1999, pages 670-674.
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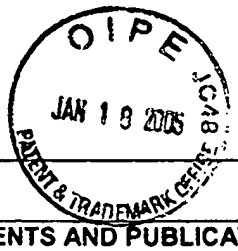

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE 09/30/2003	GROUP ART UNIT 2186
(use several sheets if necessary)	APPLICANT(S): So et al.	

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES	TRANSLATION NO

EXAMINER INITIAL		OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

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